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Personal Information

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Foreign Languages

English, B2 Upper Intermediate

Dissertations

Doctorate, Design and control of a multilevel inverter for electric vehicles using a single dc source, Van Yüzüncü Yıl University, Mühendislik Fakültesi, Elektrik Elektronik Mühendisliği Bölümü, 2023

Postgraduate, H-köprü sürücü tabanlı motor hız kontrolü ve uygulaması, Van Yüzüncü Yıl University, Fen Bilimleri Enstitüsü, Elektrik-Elektronik Mühendisliği (YI) (Tezli), 2018

Research Areas

Electrical and Electronics Engineering, Power Electronics, Electric Motor Drivers, Engineering and Technology

Academic Titles / Tasks

Research Assistant, Van Yüzüncü Yıl University, Mühendislik Fakültesi, Elektrik Elektronik Mühendisliği, 2016 - Continues

Published journal articles indexed by SCI, SSCI, and AHCI

- I. **Design of a 21-level multilevel inverter with minimum number of devices count**
Karakılıç M., Hataş H., Nuri Almalı M.
International Journal of Circuit Theory and Applications, vol.51, no.12, pp.5705-5723, 2023 (SCI-Expanded)
- II. **Design and control of a novel topology for multilevel inverters using high frequency link**
Hataş H., Almalı M. N.
ELECTRIC POWER SYSTEMS RESEARCH, vol.221, no.109458, pp.1-12, 2023 (SCI-Expanded)
- III. **Design and control of bypass diode multilevel inverter using a single DC source**
Hataş H., Almalı M. N.
Electric Power Systems Research, vol.216, pp.1-12, 2023 (SSCI)

Articles Published in Other Journals

- I. **SPEED CONTROL OF DC MOTOR USING FPGA**

Genç N., Hataş H.

International Journal on Technical and Physical Problems of Engineering (IJTPE), vol.37, no.10, pp.59-64, 2018
(Peer-Reviewed Journal)

Books & Book Chapters

I. Design and Control of a Multilevel Inverter for Electric Vehicles Using a Single DC Source

Hataş H., Almalı M. N.

in: INNOVATIVE RESEARCH IN ENGINEERING, Asst. Prof. Umut Ozkaya, Editor, duvar yayınevi, Ankara, pp.201-208, 2023

Refereed Congress / Symposium Publications in Proceedings

I. Servis Küp Uydusunun Bulanık Denetleyici Kullanılarak Hedef Küp Uyduya Yönlendirilmesi

Silahtar O., Hataş H., Zabun M., Almalı M. N., Atan Ö.

1 st International Conference on Modern and Advanced Research, Konya, Turkey, 29 - 31 July 2023, vol.1, no.1, pp.413-422

II. FPGA Implementation of 5-level Neutral Point Clamp Inverter

Hataş H., Silahtar O., Zabun M., Almalı M. N., Atan Ö.

1 st International Conference on Modern and Advanced Research, Konya, Turkey, 29 - 31 July 2023, vol.1, no.1, pp.407-412

III. Design and Control of 81-level Inverter Using Single Source for Electric Vehicles

Hataş H., Almalı M. N., Karakılıç M.

1ST INTERNATIONAL CONFERENCE ON SCIENTIFIC AND ACADEMIC RESEARCH, Konya, Türkiye, Konya, Turkey, 10 - 13 December 2022, pp.369

IV. A Novel Single Phase Low Frequency Switching Based Multilevel Inverter with Reduced Device Count

Karakılıç M., Almalı M. N., Hataş H.

1ST INTERNATIONAL CONFERENCE ON SCIENTIFIC AND ACADEMIC RESEARCH, Konya, Turkey, 10 - 13 December 2022, pp.370

V. FPGA Implementation of SPWM for Cascaded Multilevel Inverter by Using XSG

Hataş H., Genç N., Mamizadeh A.

4th International Conference on Power Electronics and their Applications (ICPEA), Elazığ, Turkey, 25 - 27 September 2019

VI. MODELING AND SIMULATION OF H-BRIDGE BASED DC MOTOR BY USING XSG

Hataş H., Genç N.

14th International Conference on Technical and Physical Problems of Electrical Engineering (ICTPE-2018), Nakhchivan, Azerbaijan, 15 - 17 October 2018, pp.67-71

Supported Projects

Genç N., Hataş H., Project Supported by Higher Education Institutions, H-köprü sürücü tabanlı motor hız kontrolü ve uygulaması, 2018 - 2018

Genç N., Hataş H., Üzmuş H., Project Supported by Higher Education Institutions, Dijital Kontrol Yöntemleri İle Doğru Akım (DA) Motor Hız Kontrolü, 2017 - 2018

Metrics

Publication: 12

Citation (Scopus): 10

H-Index (Scopus): 2